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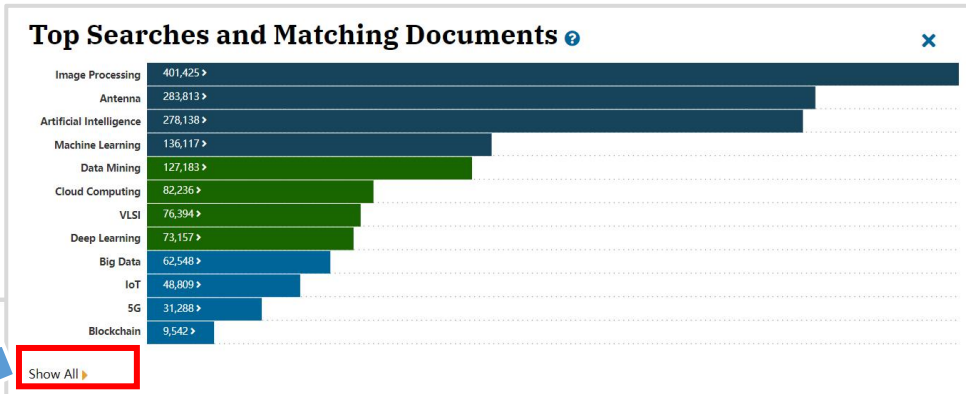
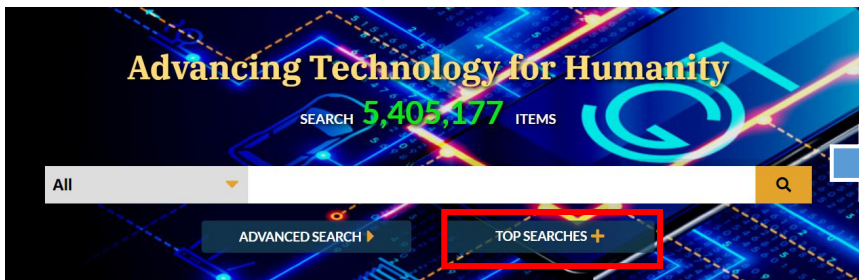
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Affiliation

Institute of Network Coding & Department of Information Engineering
The Chinese University of Hong Kong
Hong Kong, China

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Biography

Raymond W. Yeung (Fellow, IEEE) was born in Hong Kong in June 1962. He received the B.S., M.Eng., and Ph.D. degrees in electrical engineering from Cornell University, Ithaca, NY, USA, in 1984, 1985, and 1988, respectively. He was on leave at the École Nationale Supérieure des Télécommunications, Paris, France, in Fall 1986. He was a Member of Technical Staff of AT&T Bell Laboratories from 1988 to 1991. Since 1991, he has been with The Chinese University of Hong Kong, where he is currently a Choh-Ming Li Professor of information engineering and the Co-Director of the Institute of Network Coding. He has held visiting positions at Cornell University, Nankai University, Bielefeld University, the University of Copenhagen, the Tokyo Institute of ... [Show More](#)

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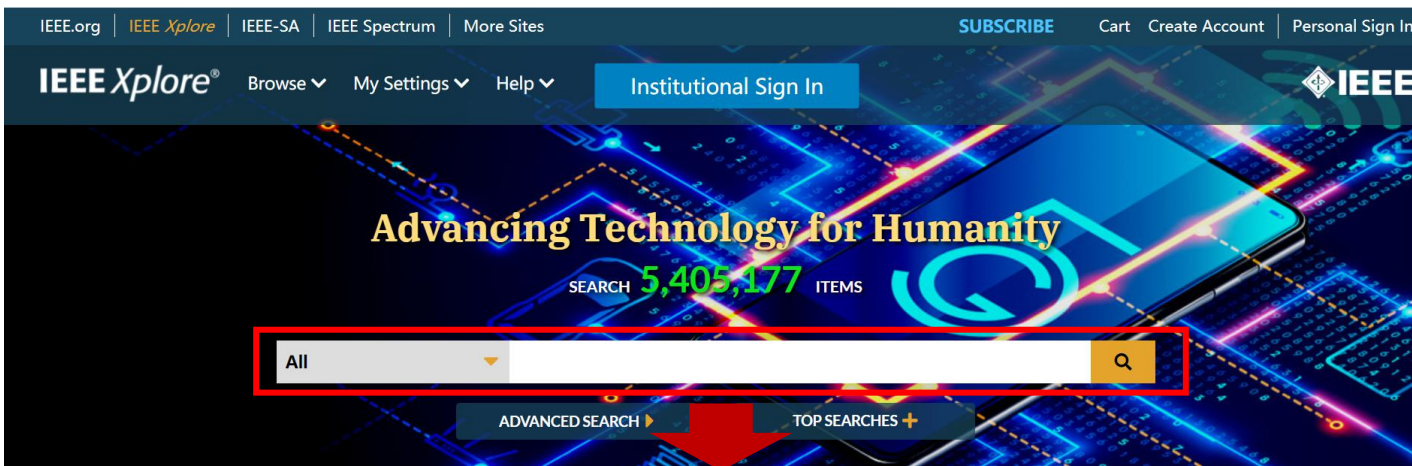
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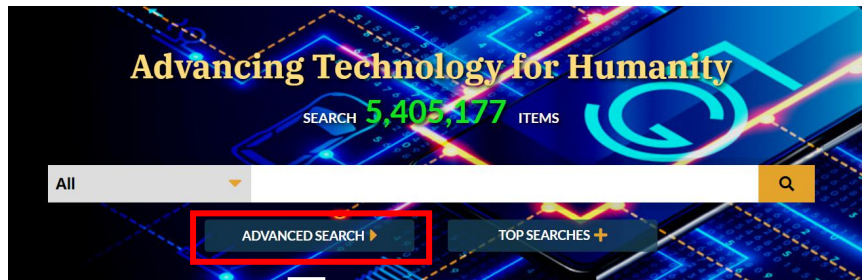
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Forward-voltage-tunable schottky-integrated trench MOSFETs 

Chiao-Shun Patrick Chuang; Kai-Yu Gary Chen; Yu-Ren Ryan Hung; Ta-Chuan Kuo; Cheng-Chin Tony Huang


2014 IEEE 26th International Symposium on Power Semiconductor Devices & IC's (ISPSD)


Year: 2014 | Conference Paper | Publisher: IEEE

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 Abstract  (1892 Kb) 

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Layout Techniques for MOSFETs 

Standards Dictionary Terms 

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Salvador Pinillos Gimenez
Year: 2016 | Book | Publisher: Morgan & Claypool

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Enhanced Electrical and Thermal Properties of Trench Metal-Oxide Semiconductor Field-Effect Transistor Built on Copper Substrate

Qi Wang; Ihsiu Ho; Minhua Li

IEEE Electron Device Letters

Year: 2009 | Volume: 30 | Issue: 1 | Journal Article | Publisher: IEEE

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Fabrication and Characterization of a Metal Oxide Semiconductor Field Effect Transistor (MOSFET)-based Micro pH Sensor

文章细节页面

Hybrid MOSFET/driver for ultra-fast switching

T. Tang; C. Burkhart

IEEE Transactions on Dielectrics and Electrical Insulation

Year: 2009 | Volume: 16, Issue: 4 | Journal Article | Publisher: IEEE

Cited by: Papers (8)

Abstract HTML

Hybrid MOSFET/driver for ultra-fast switching

Publisher: IEEE

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T. Tang; C. Burkhart All Authors

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I. Introduction

II. Design

III. Results

IV. Conclusion

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Citations

Keywords

Metrics

Abstract:

The ultra-fast switching of power MOSFETs, in about 1 ns, is very challenging due to the parasitic inductance that is intrinsic to commercial packages used for power MOSFET drivers. Parasitic gate and source inductance not only limit the voltage rise time of the internal gate structure but can also cause the gate voltage to oscillate. This paper describes a hybrid approach that substantially reduces the parasitic inductance between the driver and MOSFET gate, as well as between the MOSFET source and its external connection. A flip-chip assembly is used to directly attach a die-form power MOSFET and driver on a PCB. The parasitic inductances are significantly reduced by eliminating bond wires and minimizing lead length. The experimental results demonstrate ultra-fast switching of the power MOSFET with excellent control of the gate-source voltage.

Published in: IEEE Transactions on Dielectrics and Electrical Insulation (Volume: 16, Issue: 4, August 2009)

Page(s): 967 - 970

INSPEC Accession Number: 10847239

HTML交互式在线阅读

SECTION I. Introduction

Power MOSFETs have great potential as switches for high speed high voltage applications like pulsed power. The theoretical carrier transit time from drain to source is on the order of 200 ps in any cell of the silicon die [1]. Although the power MOSFET is

More Like This

Coreless printed circuit board (PCB) transformers for power MOSFET/IGBT gate drive circuits
IEEE Transactions on Power Electronics
Published: 1999

The reliability of high-lead solder joints in flip-chip devices
2014 15th International Conference on Electronic Packaging Technology
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Conferences > 2017 IEEE Applied Power Elect...

dv/dt Immunization limit of LV MOSFET in cascode GaN FET and dv/dt safe chart for MOSFETs

Publisher: IEEE

Cite This

PDF

Z. Chen ; Jaume Roig Guitart **All Authors**

5
Paper
Citations

612
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References

Citation Map

1. R. Mitova and R. Ghosh, "Investigations of 600V GaN HEMT and GaN diode for the power converter applications", *IEEE Trans. Power Electron.*, vol. 29, no. 5, pp. 2441-2452, May 2014.
[Show in Context](#) [View Article](#) [Full Text: PDF \(1672KB\)](#) [Google Scholar](#)
2. "Application Advantages and Disadvantages of Modern Fast Switching MOSFETs in VRM", *PCIM Europe*, 2016.
[Show in Context](#) [Google Scholar](#)
3. Christian R. Müller and Stefan Buschhorn, "Impact of module parasitics on the performance of fast switching devices", *PCIM Europe*, 2014.
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4. Alan Elbanhawy, MOSFET Susceptibility to Cross Conduction, Power Electronics Technology, April 2005.
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5. Alan Elbanhawy, AN-7019 Limiting Cross-Conduction Current in Synchronous Buck Converter Designs, Fairchild Semiconductor.
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6. Toni López I, Reinhold Efferich I and Eduard Alarcón, *Voltage Regulators for Next Generation* *Microprocessors* ISBN:978-1-4419-7550-1

More Like This

Requirements for low intermodulation distortion in GaN-Al/sub x/Ga/sub 1-x/N high electron mobility transistors: a model assessment
IEEE Transactions on
Published: 2002

Quantitative Discussion of Terahertz Waves of D-MOSFET and High-F

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1. Masahiro Koyama, Kentaro Ikeda, Kazuto Takao, "Novel cascode GaN module integrated a single gate driver IC with high switching speed controllability", *Power Electronics and Applications (EPE'18 ECCE Europe) 2018 20th European Conference on*, pp. P.1-P.8, 2018.
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2. Tianhua Zhu, Fang Zhuo, Feng Wang, Hailin Wang, Xiaoping Sun, Shuhuai Shi, Baohui Ma, "Quantitative Analysis and Suppression Strategies of Dv/dt Induced Turn-on of Cascode GaN FETs in Half-bridge Circuits", *Wide Bandgap Power Devices and Applications in Asia (WIPDA Asia) 2018 1st Workshop on*, pp. 130-134, 2018.
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3. Tianhua Zhu, Fang Zhuo, Fangzhou Zhao, Feng Wang, Tong Zhao, "Quantitative Model-Based False Turn-on Evaluation and Suppression for Cascode GaN Devices in Half-Bridge Applications", *Power Electronics IEEE Transactions on*, vol. 34, no. 10, pp. 10166-10179, 2019.
[Show Article](#) [Full Text: PDF \(8243KB\)](#) [Google Scholar](#)
4. Jian Chen, Xiong Du, Quanming Luo, Xinyue Zhang, Pengju Sun, Lin Zhou, "A Review of Switching Oscillations of Wide Bandgap Semiconductor Devices", *Power Electronics IEEE Transactions on*, vol. 35, no. 12, pp. 12182-12190, 2020.

References

Citations

文章细节页面- 作者介绍

Analytical Modeling and Experimental Validation of Threshold Voltage in BSIM6 MOSFET Model

Publisher: IEEE

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PDF

Harshit Agarwal ; Chetan Gu

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2009
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III. Threshold Voltage
Model

IV. Simulation Results

V. Conclusion

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Figures

References

Citations



Chenming Hu

Department of Electrical Engineering and Computer
Science, University of California, Berkeley, Berkeley, CA,
USA

了解作者详情

Chenming Hu (F' 03) is the TSMC Distinguished Professor Emeritus of University of California Berkeley, Berkeley, CA, USA. He is a Former Chief Technology Officer of TSMC. He is a Board Director of SanDisk Inc., and of the non-profit Friends of Children with Special Needs. He is well known for his work on the 3-D transistor, FinFET, which can be scaled to single digit nanometers. He has developed widely used IC reliability models and led the research of BSIM—the first industry—standard SPICE model used by most IC companies to design CMOS products since 1996. He was a recipient of the IEEE Andrew Grove Award, the Solid State Circuits Award and Nishizawa Medal, the Kaufman Award of the EDA industry, the University Research Award of the U.S. Semiconductor Industry Association, and the UC Berkeley's Highest Honor for teaching—the Berkeley Distinguished Teaching Award.

More Like This

Understanding quasi-ballistic transport
in nano-MOSFETs: part II—Technology
scaling along the ITRS
IEEE Transactions on Electron Devices
Published: 2005

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Chenming Hu

Also published under: C. Hu

Affiliation
Department of Electrical Engineering and Computer Science
University of California Berkeley
Berkeley, CA, USA

Publication Topics
MOSFET, semiconductor device models, field effect transistors, technology CAD (electronics), silicon-on-insulator, ferroelectric materials, silicon, 1/f
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Biography
Chenming Hu (LF' 03) is currently a Professor of Electrical Engineering and Computer Science at the University of California at Berkeley, Berkeley, California, Berkeley, CA, USA, and Friends of the Earth International. He is also known for his involvement in FinFET—the industry standard transistor manufacturing technology for 28 nm and 20 nm products. *(Based on document)*

Publications **79**

Citations **986**

Publications by Year



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Co-Authors:

M. Abu-Rahma
Amit Agarwal

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Kai-Shin Li; Yun-Jie Wei; Yi-Ju Chen; Wen-Cheng Chiu; Hsiu-Chih Chen; Min-Hung Lee; Yu-Fan Chiu; Fu-Kuo Hsueh; Bo-Wei Wu; Pin-Guang Chen; Tung-Yan Lai; Chun-Chi Chen; Jia-Ting Chen; Ming-Hsiung Wu; Chih-Chieh Chang; Chih-Chieh Chang; Chih-Chieh Chang

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A Voting-Mechanism based Ensemble Framework for Constraint Handling Techniques 
Guohua Wu; Xupeng Wen; Ling Wang; Witold Pedrycz; P. N. Suganthan
IEEE Transactions on Evolutionary Computation
Year: 2021 | Early Access Article | Publisher: IEEE

▶ Abstract



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Description

This is the supplementary file of the article "A Voting-Mechanism based Ensemble Framework for Constraint Handling Techniques" published in IEEE Transactions on Evolutionary Computation. This file contains two parts. One part includes the details of the 57 real-world constrained optimization problems, which are used in Section IV in the manuscript. Another part is the experimental results, including the best/mean/median values of the ten comparison algorithms on the 57 real-world constrained optimization problems, as the supplementary data of Table I and Table II in the manuscript.

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SR Latch: The Wrong Introduction to Digital Memory
Abdulahdi Shoufan
2020 IEEE International Symposium on Circuits and Systems (ISCAS)
Year: 2020 | Conference Paper | Publisher: IEEE

Abstract ((html)) (128 Kb) **Video**

会议视频

The screenshot shows a video player interface. The main content is a presentation slide titled "Open Source RFNoC-Based Testbed for Millimeter-Wave Experimentation Using USRP Software Defined Radios" by Adriana Moreno, Jesús Omar Lacruz, and Joerg Widmer. The slide is from the 2020 IEEE International Symposium on Circuits and Systems (ISCAS) Virtual, October 10-21, 2020. The video player includes a transcript on the right side, which contains the following text:

Transcript

Open Source RFNoC-Based Testbed for Millimeter-Wave Experimentation using USRP Software Defined Radios

[00:03] JESUS OMAR LACRUZ Hello. I am Jesus Omar Lacruz from IMDEA Network Institute, Madrid, Spain. I will be in charge to present our work in the 2020 International Symposium On Circuits and Systems. This work is entitled "Open source RFNoC-based testbed for millimeter-wave experimentation using USRP software defined radios". As an innovative technology, millimeter-wave communication requires suitable testbed platforms to [?] speed up [?] data collection and validation of new proposals.

[00:38] JESUS OMAR LACRUZ If we list the [INAUDIBLE] characteristics of a testbed, we'll always [INAUDIBLE] flexibility, the configurability, easy to adapt to different conditions, and of course, affordability. We can find different solutions for millimeter-wave testbed with different characteristics that made them ideal for different scenarios. Some works use commercial off-the-shelf devices as research platforms.

[01:06] JESUS OMAR LACRUZ The main problem is the lack of access to physical layer information. On the other hand, commercial testbeds involve prices that could be not affordable for all research groups. Then we found that USRPs has proven efficiency in sub-5-gigahertz network. So using it in millimeter-wave systems will bring the desired flexibility, affordability, and a wide online open-source community.

[01:35] JESUS OMAR LACRUZ Besides enhancing its functionality with RFNoC framework [INAUDIBLE] the implementation of signal processing blocks in the FPGA, which is very important to reduce latency and validate system in a hardware-in-the-loop manner. Keeping this in mind, in this work we designed and implemented a millimeter-wave experimentation platform using USRPs and 60-gigahertz transceivers. We take advantage on the RFNoC framework to implement the hardware processing blocks to process the preamble of IEEE 802.11ad compliant frames in real-time working at a

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 - Code (356)
- Conference Location

Personalized Channel Recommendation Deep Learning From a Switch Sequence
Can Yang ; Sixuan Ren ; Yong Liu ; Houwei Cao ; Qihu Yuan ; Guoqiang Han
IEEE Access
Year: 2018 , Volume: 6
Page s: 50824 - 50838
IEEE Journals & Magazines
▶ Abstract (html) PDF (11429 Kb) © **Datasets**

数据

CHANNELS SWITCH SEQUENCES OF 300 IPTV VIEWERS IN A MONTH

Citation Author(s): Sixuan Ren and Can Yang in South China University of Technology
Submitted by: Can Yang
Last updated: Thu, 11/08/2018 - 10:34
DOI: 10.21227/H2396N
Data Format: TXT
License: Creative Commons Attribution © ⓘ
Dataset Views: 196

CATEGORIES

- > Communications
- > Discrete-time signal processing
- > Other

KEYWORDS

- > IPTV, Recommender System, Machine Learning

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Abstract:

This dataset includes the Channels Switch Sequences of 300 IPTV viewers in Guangzhou, P.R. China, in August, 2014. There are 4 columns in the file, which represent viewer ID, the current channel number, the next channel number, the date of the month, respectively. The first column, the ID code of a viewer, ranks in descent with the times the viewer watched tv channels. The more times a viewer watches tv channels, the bigger the ID is. In a day, the rows are time series and generated step by step as the real watching tv behavior.

DATASET FILES

- > IPTVChannelSwitchSequencesUsers300.txt (3.91 MB)

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- Video (897)
- Datasets (451)
- Code (356)
- Conference Location

Optimal microgrids placement in electric distribution systems using complex network framework

Mahmoud Saleh ; Yusef Esa ; Nwabueze Onuorah ; Ahmed A. Mohamed
2017 IEEE 6th International Conference on Renewable Energy Research and Applications (ICRERA)
Year: 2017
Page s: 1036 - 1040
Cited by: Papers (2)
IEEE Conferences

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代码



Code & Datasets

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Code: Applications of Complex Network Analysis in Electric Power Systems

MATLAB

```
Applications of Complex Network ... (Mahmou... )
```

Files

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```
1 #!/usr/bin/env bash
2 matlab -nodisplay -nosplash -
  nosoftwareopengl -r
  "addpath(genpath('/code'));
  ComplexNetworkAnalysis_for_ElectricP
  owerSystems();"
3
```

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IEEE Electron Device Letters
Year: 2009 | Volume: 30, Issue: 1 | Journal Article | Publisher: IEEE
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Advanced Search ?

The screenshot displays the IEEE Xplore search interface. At the top, there are navigation tabs for 'Advanced Search', 'Command Search', and 'Citation Search'. A search bar contains the query '"National Univ* of Defense Tech"'. Below the search bar, there are options for 'OR' and 'AND' search terms. The search results show 12,015 items, with a filter for 'Author Affiliations' set to '"National Univ* of Defense Tech" OR ("Author Affiliations...")'. A 'Set Search Alerts' dialog box is open, allowing the user to set a search alert. The dialog box includes a 'Search Alert Name*' field, an 'Email Address' field (with 'erin@igroup.com.cn' entered), and 'Cancel' and 'Save' buttons. The background shows a list of search results, including a paper titled 'Data-Driven Intelligent Transportation Systems: A Survey' by Junping Zhang, Fei-Yue Wang, Kunfeng Wang, Wei-Hua Lin, and Xin Xu, published in IEEE Transactions on Intelligent Transportation Systems in 2011.

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Junping Zhang; Fei-Yue Wang; Kunfeng Wang; Wei-Hua Lin; Xin Xu; Cheng Chen
IEEE Transactions on Intelligent Transportation Systems
Year: 2011 | Volume: 12, Issue: 4 | Journal Article | Publisher: IEEE
Cited by: Papers (772)

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Image formation techniques for Vehicle-Mounted Forward Looking Ground Penetrating SAR

Jian Wang; Yanghuan Li; **Zhimin Zhou**; Tian Jin; Yanguang Yang; Yuming Wang

2008 International Conference on Information and Automation
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Zhimin Zhou

Also published under: Z. M. Zhou, Zhi-Min Zhou, Zhi-min Zhou, Z. Zhou

Affiliation
National University of Defense Technology
College of Electronic Science and Technology
Changsha, China

Publication Topics
radar imaging, synthetic aperture radar, ultra wideband radar, image resolution, radar detection, remote sensing by radar, Global Positioning System, airborne radar, frequency-
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Biography
Zhimin Zhou received the B.S. degree in aeronautical radio measurement and control and the M.S. and Ph.D. degrees in information and communication engineering from the National University of Defense Technology (NUDT), Changsha, China, in 1982, 1989, and 2002, respectively. He is currently a Professor with NUDT. His research interests include ultra-wideband radar system and real-time signal processing. Dr. Zhou is a Fellow of the Chinese Institute of Electronics. *(Based on document published on 13 June 2019).*

Publications **Citations**

130 **923**

Publications by Year

Co-Authors:

Raja Syamsul Azmir Raja Abudullah
Biyang Lu
Yu-lin Chang
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